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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 6780

S. MASAKI

Examiner: P. Wamsley

Application No.: 10/705,977

Art Unit: 2819

Filed: November 13, 2003

Attorney Dkt. No.: 100021-00134

For: MULTIPLEXER CIRCUIT FOR CONVERTING PARALLEL DATA INTO SERIAL DATA AT HIGH SPEED AND SYNCHRONIZING THE SERIAL DATA WITH A CLOCK SIGNAL

**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: June 13, 2005

Sir:

In response to the Office Action dated March 24, 2005, please amend the application referenced above as follows.

**Amendment to the Claims** begins on page 2 of this paper.

**Remarks and Arguments** begins on page 7 of this paper.